

# TATA INSTITUTE OF FUNDAMENTAL RESEARCH

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Date: 19-11-2020

**Public Tender Ref. No.** TIFR/PD/CA20-27/200399

**Sub:** CORRIGENDUM NO. 2, AGAINST TENDER NO. TIFR/PD/CA20-27/200399 dated 11-11-2020 for Supply, Installation and Commissioning of High-Performance Computing (HPC) Cluster for Tata Institute of Fundamental Research, Hyderabad.

Dear Bidders/Vendors,

Please refer the subject tender published on 12-11-2020 and subsequent to the pre-bid meeting held on dated 19-11-2020, the following corrigendum to the subject tender is being issued:

S.No.	Clarification/amendment sought by the bidders	Amendments/clarifications
1.	Annexure - A Page no 2. Clause 2. Requesting to consider bidders with global experience as well.	Accepted and amended. Read as follows,  The Bidder should have the experience in building HPC Clusters in the worldwide HPC market since last years or in the Indian HPC Market since last 3 years with a logistics facility for the bidding OEM in India for easy access and availability of spares and to ensure the proper back-end support for smooth execution and post-sale support operations. Documentary proofs should be attached.
2.	Annexure - A Page no 4 and 5, S.no 1 and 2. Requesting to consider memory requirement in 2 powers for better memory optimization.	Accepted and amended. Read as follows,  Please consider minimum 256 GB DDR4 ECC RAM for master + storage node and 128 GB DDR4 ECC RAM for compute nodes with or better in a fully balanced configuration and all the memory channels to its available full bandwidth should be populated.

3.	Annexure - A Page no 4 and 5, S.no 2. Requesting not to allow source code modification of HPL linpack binary to achieve the requested realtime sustained peak Tera Flops (TF) of 2.5 TF using the source from the following link - <a href="https://www.netlib.org/benchmark/hpl/">https://www.netlib.org/benchmark/hpl/</a>	Accepted.  The bidders are not allowed to change the source code HPL linpack files from netlib and if they are using commercial/proprietary compilers for achieving those results, they have to provide free site license for such compilers with support till the mentioned warranty period.
4.	Annexure -A Page no 6, S.No 3 item 2  Only Infiniband/Omnipath is allowed or Other networking technologies used to achieve the same performance will be allowed?	Accepted.  Other networking technologies like RDMA will also be allowed, if they are also able to achieve the same performance provided by Infiniband/Omnipath
5.	Payment Terms for INR Bids:  Requested for 80% payment against material receipt and balance 20% after installation.	Accepted the following payment term for Total / Full INR bids:  80% payment shall be released after receipt of complete material as per purchase order in a single lot and acceptance of the same at TIFR Hyderabad and balance 20% payment shall be released after installation, commissioning and acceptance of the equipment at TIFR Hyderabad Campus and on submission of BG @ 10% until completion of warranty period plus 60 days.  Partial shipment for foreign bids / partial delivery of material for full INR bids will not be accepted by TIFR Hyderabad.

All other terms & conditions of subject tender shall remain unchanged. This Corrigendum No. 02 is an integral part of the subject tender and a copy of the same must be submitted along with the offer duly signed and stamped.

**ADMINISTRATIVE OFFICER  
(PURCHASE SECTION)  
TIFR, HYDERABAD**