

TATA INSTITUTE OF FUNDAMENTAL RESEARCH

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Date: 23-11-2021

Global Tender Ref. No. TIFR/PD/GT/CA21-153/211033.

Sub: CORRIGENDUM No. 1 against Tender Ref. No. TIFR/PD/GT/CA21-153/211033 dated 16-11-2021 for Supply, Installation and Commissioning of High-Performance Computing Cluster for TIFR Hyderabad.

Dear Bidders/Vendors,

Please refer the subject tender published on 17-11-2021, the following corrigendum to the subject is being issued:

S.No	Clarification Sought	Clarification/Amendment
1.	Requesting to consider the global HPC experience/Industrial experience and amend technical prequalification criteria point no.1 on page no -3	<p>Amended. Read as follows</p> <p>The bidder should have executed at least three projects using architecture and technologies similar to those being proposed in their quotation against this tender. In addition, the following condition should also be satisfied.</p> <ul style="list-style-type: none">• At least one installation of minimum 30 TF (Tera Flops) or• At least two installations of 25 TF or• At least three installations of 20 TF <p>Note: All the mentioned peak value should be CPU only sustained peak performance ratings, which should be a cluster (more than one node with interconnects). GPU Servers and Single servers will not be considered. Purchase order copies of the same must be submitted with the technical bid.</p> <p>The bidders with domestic experience should have successfully executed projects at Government/ Public/ Private Research organizations /institutions /Manufacturing industries performing scientific computation using High Performance Computing Clusters across the country within the last five years.</p>

		<p>The bidders with global experience should have registered offices and service centres in south India within 1000 KM radius from TIFR-H and they should have installed minimum 5 Nos of HPCs, which should have been listed in the world top supercomputing listing published by https://top500.org/</p> <p>Purchase order with final commissioning report with the mention of compute capacity in TF and architecture details duly signed by customer should be submitted with the technical bid.</p>
2.	Please clarify on the minimum clock speed of processor mentioned in Master + storage node and compute nodes, whether it is the base clock speed or turbo speed?	The minimum clock speed is the base clock speed of the processors.
3.	Please share the list of Open source Application which needs to configure in this new cluster	MPI, LAMMPS, Quantum Espresso, etc., or any other open source software required by the user
4.	Shall we consider RACK, KVM, and Console etc.?	No. TIFR-H has modular chilled water cooled 42U Rittal server rack and we will provide the required infrastructure for hosting the HPC.
5.	Requesting 26 - 30 Weeks for product delivery and installation.	Amended. Read as follows Delivery period should be within 16 weeks from the date of purchase order. Once delivered to onsite, the installation, commissioning and acceptance testing period will be within 2 weeks from the date of delivery of equipment.

All other terms & conditions of subject tender shall remain unchanged. This Corrigendum No. 01 is an integral part of the subject tender and a copy of the same must be submitted along with the offer duly signed and stamped.

**ADMINISTRATIVE OFFICER
(PURCHASE SECTION)
TIFR, HYDERABAD**