TATA INSTITUTE OF FUNDAMENTAL RESEARCH HYDERABAD

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Date: 19-01-2023

Public Tender Ref. No. TIFR/PD/CA22-21/22-23/M/021

Sub: CORRIGENDUM NO. 1, Against Tender Ref. No. TIFR/PD/CA22-21/22-23/M/021 dated 11-01-2023 for Supply, Installation and Commissioning of Intel Compute HPC Servers to TIFR Hyderabad.

Dear Bidders/Vendors,

Please refer the subject tender published on 11-01-2023, the following corrigendum to the subject is being issued:

1) Technical Corrigendum

S.No	Clarification Sought	Clarification/Amendment
1.	Page No – 5 item no - 1 Each server should have minimum 96 or better Intel Xeon Cooper lake CPU Cores or higher with a minimum base clock speed of 3.0 GHz or better. The processors Must have Native support of AVX 512 instructions or better. Each node should have a memory of minimum 512 GB DDR4 ECC RAM or better configurations. RAM speed should be the maximum memory speed supported by the quoted CPUs. The bidders should run linpack on the nodes for 24 hours and submit the results as a part of commissioning. Along with the CPU ratings, the nodes should have the following configurations or better.	Read as follows. Each server should have minimum 96 or better Intel Xeon Cooper Lake/ Sapphire Rapids CPU Cores with a minimum base clock speed of 2.1 GHz or better. The processors Must have Native support of AVX 512 instructions or better. Each node should have a memory of minimum 512 GB DDR (4 or 5) ECC RAM or better configurations. RAM speed should be the maximum memory speed supported by the quoted CPUs. The bidders should run linpack on the nodes for 24 hours and submit the results as a part of commissioning. Along with the CPU ratings, the nodes should have the following configurations or better.
2.	Page no -5, item no -1, point no - 1 CPU Details – 4 x Intel Xeon 8360 H 24 C, 3.0 GHz	Read as follows CPU Details - 96 or better Intel Xeon Cooper Lake/Sapphire Rapids CPU Cores or higher with a minimum base clock speed of 2.1 GHz or better.
3.	Page no -5, item no -1, point no - 2 Memory Details – 512 GB (16 x 32 GB) DDR4 3200 MHz ECC RAM or better	Read as follows Memory Details – 512 GB (16 x 32 GB) DDR (4 or 5) ECC RAM or better. RAM speed should be the maximum memory speed supported by the quoted CPUs.

4.	For bidders who are quoting Intel Sapphire Rapids CPUs	The quoted processors should match the mentioned core count per server and also the quoted processor's linpack efficiency should be equal or greater than the equivalent cooper lake processor based server. The same should be demonstrated at the time of commissioning of the server.

Note: Remaining technical specifications, terms and conditions remains the same as per the
      tender.

2) The due date is amended as "08-02-2023 upto 13.00 Hrs." instead of "01-02-2023 upto 13.00 Hrs." The technical bids of the same will be opened on "08-02-2023 at 15.00 Hrs."

All other terms & conditions of subject tender shall remain unchanged. This Corrigendum No. 01 is an integral part of the subject tender and a copy of the same must be submitted along with the offer duly signed and stamped.

ADMINISTRATIVE OFFICER
(PURCHASE SECTION)
TIFR, HYDERABAD