

टाटा मूलभूत अनुसंधान संस्थान हैदराबाद
TATA INSTITUTE OF FUNDAMENTAL RESEARCH HYDERABAD
प्लॉट नंबर 36/पी, गोपनपल्ली गांव, सेरिलिंगमपल्ली मंडल, रंगारेड्डी जिला,
हैदराबाद 500 046, तेलंगाना, भारत.
Plot No.36/P, Gopanpally Village, Serilingampally Mandal, RangaReddy District
Hyderabad 500 046, Telangana, India.

टेलीफोन /Telephone: +91-40-2020 3040

ईमेल /Email: purchasegroup@tifrh.res.in

वेबसाइट /Website: www.tifrh.res.in

तारीख /Date: 11-08-2026

Ref. No. TIFR/PD/CA26-03/M/03

Sub: Pre-Bid Meeting Clarification for the Public Tender for Supply, Installation and Commissioning of High Performance Computing Cluster (HPCC) at TIFR Hyderabad.

Dear Bidders/Vendors,

With reference to above-mentioned tender, the pre-bid meeting was scheduled on 07-08-2026 at 11.00 hrs.

Pre-Bid Meeting Participation:

- 1) M/s. Meganet Technologies Global Limited.
- 2) M/s. Micropoint Computers Pvt Ltd.
- 3) M/s. Netweb Technologies India Limited.
- 4) M/s. Dell Technologies

Technical and Commercial Clarifications:

- 1) Page no -3 Point no - 1 Requesting to revise and reduce the prequalification criteria as follows

The bidders should have executed successful installations at Government/ Public/ Private Research organizations /institutions /Manufacturing industries using architecture and technologies to those being proposed in their quotation against this tender across the country.

In addition, the following condition should also be satisfied.

At least one installation of minimum 70% or

At least two installations of each 50% or

At least three installations of each 30% of the tender value

Purchase order with final commissioning report with the mention of compute architecture details duly signed by customer should be submitted with the technical bid. The contact details of the customer's technical person should be provided. TIFR-H may independently contact them to verify the claim.

Response: The bidders should have executed successful installations at Government/ Public/ Private Research organizations /institutions /Manufacturing industries using architecture and technologies to those being proposed in their quotation against this tender across the country.

In addition, the following condition should also be satisfied.

At least one installation of minimum 70% or

At least two installations of each 60% or

At least three installations of each 50% of the tender value

Purchase order with final commissioning report with the mention of compute architecture details duly signed by customer should be submitted with the technical bid. The contact details of the customer's technical person should be provided. TIFR-H may independently contact them to verify the claim.

2) Page no -4 Point no - 4

Requesting to revise the clause as follows

The Bidder and OEM should have a registered office in India, and the bidder/OEM should have service support available with L1/L2 engineers at Hyderabad in the relevant field of quoted item. Details of the same should be enclosed..

Response: Accepted. Read as follows

The Bidder and OEM should have a registered office in India, and the bidder/OEM should have service support available with L1/L2 engineers at Hyderabad in the relevant field of quoted item. Details of the same should be enclosed.

3) Page no - 6 Item no - 1 Point no - 1 and Page No - 7 Item no 2 point no - 1

Requesting the Processor generation to be 4th or higher and RAM to DDR5 4800 MHz or higher

Response: No Change.

4) Page no - 6 Item no - 1 Point no - 2 and Page no - 7 Item no - 2 Point no - 2

Requesting the Memory Details to be amended as follows – 256 GB DDR4 ECC RAM. Memory speed – Maximum memory speed supported by the quoted CPUs.

Response: Accepted. Read as follows

Memory Details – 256 GB DDR4 ECC RAM or better. Memory speed – Maximum memory speed supported by the quoted CPUs.

5) Page no - 7 Item no - 3 Point no -1

Request to correct the typo on the Memory generation from DDR4 to DDR5

Response: Accepted. Read as follows

Each compute nodes should have minimum 256 or better AMD EPYC 4th generation CPU Cores or higher with a minimum base clock speed of 2.25 GHz or better. The processors Must have Native support of AVX 256 instructions or better.

Each node should have a memory of minimum 512 GB DDR5 ECC RAM or better. RAM speed should be the maximum memory speed supported by the quoted CPUs.

The bidders should run linpack across all nodes for 24 hours and submit the results as a part of commissioning. Along with the CPU ratings, the nodes should have the following configurations or better.

6) Page no - 8 Item no - 4 point no - 4

Request to amend as follows

2 x 960 GB Enterprise 3 DWPD SAS NVMe/SSD configured in RAID 1 for L2ARC (dedicated to each server)

Response: Accepted. Read as follows

2 x 960 GB Enterprise 3 DWPD SAS NVMe/SSD configured in RAID 1 for L2ARC (dedicated to each server)

7) Page no - 8 Item no - 4 point no - 4,5 and 7

Request to permit the use of 1 DWPD Enterprise SAS/NVMe SSDs in place of 3 DWPD Enterprise SAS/NVMe SSDs for the Special VDEVs, as lower- capacity 3 DWPD enterprise SSDs are no longer readily available in the market. Furthermore, it is requested that the specification define the overall usable capacity required for the Special VDEVs rather than prescribing a fixed individual drive capacity. This will provide vendors with the flexibility to propose appropriate enterprise-grade SSD capacities based on current market availability while ensuring that the required Special VDEV capacity, performance, endurance, and redundancy requirements are fully met.

Response: Accepted. Read as follows

2 x 960 GB Enterprise 1 DWPD or higher SAS/NVMe SSD configured in RAID 1 for L2ARC (dedicated to each server)

2 x 480 GB Enterprise 1 DWPD or higher SAS/NVMe SSD configured in RAID 1 for ZIL (dedicated to each server) achieve minimum 1. 92 TB raw capacity special VDEVs using Enterprise 1 DWPD or higher SAS/NVMe SSD configured in RAID 1 mirror pairs for special vdevs. (In case of NVMe, the bidders has to consider the associated accessories to achieve multipath and dual-controller active/passive HA, or bidders can quote active/standby model without JBOF and accessories, where the bidders have to manually import the pools to the second controller in case of failure on the primary controller)

8) Page no -8 Item no - 4 point no - 7

Requesting to allow NVMe drives along SAS SSDs for special vdevs.

Response: Accepted. Read as follows

achieve minimum 1. 92 TB raw capacity special VDEVs using Enterprise 1 DWPD or higher SAS/NVMe SSD configured in RAID 1 mirror pairs for special vdevs. (In case of NVMe, the bidders has to consider the associated accessories to achieve multipath and dual controller active/passive HA)

9) Page no -8 Item No -4 point no -1

As the availability of SAS SSDs is very limited. Therefore, it is requested to consider a single-controller storage solution, which can be proposed with NVMe drives for improved availability and performance

Response: Amended. Read as follows

2 No of controller servers each should have minimum 16 Cores Intel Xeon 5th/AMD EPYC 3rd or higher generation CPU Cores with minimum base clock speed of 2.4 GHz or better. The processors Must have Native support of AVX256 instructions or better. It should be installed and configured as either Active/Passive or Active/Standby dual controller storage cluster using latest stable open ZFS according to the user requirements.

10) Page no - 8 Item No -4 Point no - 8

Requesting to increase the JBOD form factor to 6U or better.

Response: Accepted. Amended as follows

1 x 4U 60 Bay Dual controllers JBOD or better with Redundant Power Supply with management ports configured in high availability with both controller servers. JBOD form factor should not be more than 7U.

11)Page No - 13

Requesting to increase the delivery time from 16 Weeks to 20 Weeks

Response: Accepted. Read as follows

Delivery period will be 20 weeks from the date of purchase order. Once delivered to onsite, the installation, commissioning and acceptance testing period will be within

4 weeks from the date of delivery of equipment. Delay in delivery will have penalty. TIFR-H reserves the right to cancel the order if it is not deployed even after that.

12)Page No - 18 Point no -3

Requesting to revise the bid validity from 180 to 60 days considering semiconductor market volatility

Response: Amended. Read as follows

Quotations must be valid for a period of 120 days from the date of opening the bid prescribed by the Purchaser. A bid valid for a shorter period may be rejected by the Purchaser as non responsive.

Sd/-
Head, Administration & Finance
TIFR Hyderabad